What Is Claimed Is:

1. An amplification circuit amplifying an input signal to generate an output signal, said amplification circuit comprising:

an amplifier amplifying said input signal, wherein a gain of said amplifier changes when amplifying said input signal; and

a component provided across an output of said amplifier, wherein an impedance of said component does not change when amplifying said input signal such that changes in an amplification factor provided by said amplification circuit are minimized when amplifying said input signal to generate said output signal.

2. The amplification circuit of claim 1, wherein said component comprises a resistor.

3. The amplification circuit of claim 2, wherein said amplifier contains a first output terminal and a second output terminal, and wherein said resistor is connected to both of said first output terminal and said second output terminal.

4. The amplification circuit of claim 3, further comprising a feedback circuit across said amplifier, wherein a resistance value of said resistor is chosen using the equation:

$$G300=(1/B300)/[1+\{Sc*(1+P*Vout)+S390\}*\{1+Q*Vout*(Sc+S390)/Hc\}/(Hc*B300)],$$

wherein said resistance = (1/S390), G300 represents an amplification factor of said amplification circuit, B300 represents a feedback factor of said feedback circuit, Sc, P, Q and Hc are determined by a manufacturing process used to implement said amplification circuit.

5. The amplification circuit of claim 4, wherein said resistor is integrated into said amplifier.

6. A device comprising:

an amplification circuit amplifying an input signal to generate an output signal, said amplification circuit comprising:

an amplifier amplifying said input signal, wherein a gain of said amplifier changes when amplifying said input signal; and

a component provided across an output of said amplifier, wherein an impedance of said component does not change when amplifying said input signal such that changes in an amplification factor provided by said amplification circuit are minimized when amplifying said input signal to generate said output signal.

7. The device of claim 6, wherein said component comprises a resistor.

8. The device of claim 7, wherein said amplifier contains a first output terminal and a second output terminal, and wherein said resistor is connected to both of said first output terminal and said second output terminal.

9. The device of claim 8, wherein said amplification circuit further comprises a feedback circuit across said amplifier, wherein a resistance value of said resistor is chosen using the equation:

$$G300=(1/B300)/[1+\{Sc*(1+P*Vout)+S390\}*\{1+Q*Vout*(Sc+S390)/Hc\}/(Hc*B300)],$$

wherein said resistance = (1/S390), G300 represents an amplification factor of said amplification circuit, B300 represents a feedback factor of said feedback circuit, Sc, P, Q and Hc are determined by a manufacturing process used to implement said amplification circuit.

10. The device of claim 9, wherein said resistor is integrated into said amplifier.

11. The device of claim 9, wherein said device comprises a wireless base station, said device further comprising:

an antenna receiving an external signal;

an analog processor processing said external signal to generate said input signal; and

an analog to digital converter converting said output signal to a sequence of digital codes.

12. A method of implementing an amplification circuit for amplifying an input signal to generate an output signal, said method comprising:

providing an amplifier to amplify said input signal, wherein a gain of said amplifier changes when amplifying said input signal;

providing a component across an output of said amplifier, wherein an impedance of said component does not change when amplifying said input signal such that changes in an amplification factor provided by said amplification circuit are minimized when amplifying said input signal to generate said output signal.

13. The method of claim 12, wherein said component comprises a resistor.

14. The method of claim 13, wherein said amplifier contains a first output terminal and a second output terminal, and wherein said resistor is connected to both of said first output terminal and said second output terminal.

15. The method of claim 14, further comprising a feedback circuit across said amplifier, wherein a resistance value of said resistor is chosen using the equation:

$$G300=(1/B300)/[1+\{Sc*(1+P*Vout)+S390\}*\{1+Q*Vout*(Sc+S390)/Hc\}/(Hc*B300)],$$

wherein said resistance = (1/S390), G300 represents an amplification factor of said amplification circuit, B300 represents a feedback factor of said feedback circuit, Sc, P, Q and Hc are determined by a manufacturing process used to implement said amplification circuit.